



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al.
 Serial No. : 09/504,235
 Filed : February 15, 2000

Art Unit : 2629
 Examiner : David L. Lewis
 Confirmation No.: 7610
 Notice of Allowance Date: August 25, 2006

Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

MAIL STOP ISSUE FEE

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed August 25, 2006, enclosed is a completed issue fee transmittal form PTOL-85b. A fee in the amount of \$1430 was paid on February 10, 2005 for the issue fee and patent copies. The Director of the U.S. Patent & Trademark Office is requested to apply the previously paid fee to the application identified above.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,


 John F. Hayden
 Reg. No. 37,640

Date: November 22, 2006

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